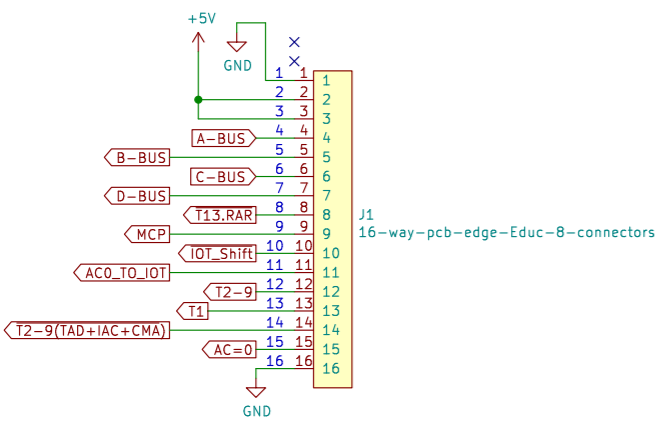
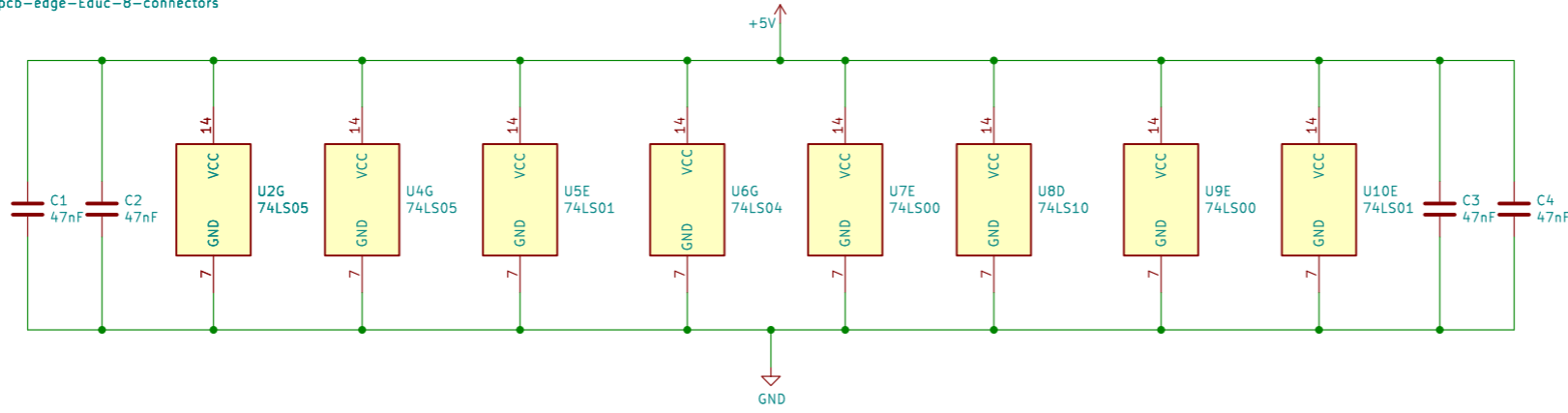
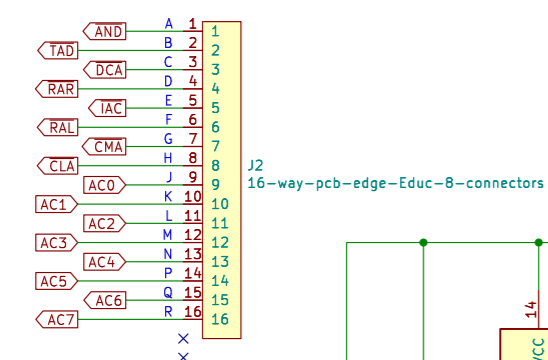
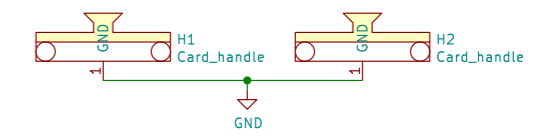
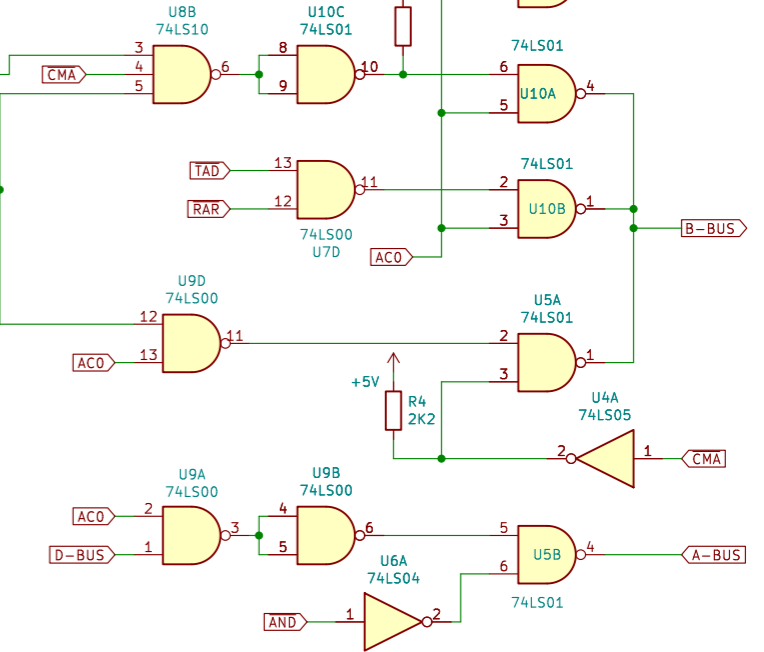


1. C-BUS not terminated on Accumulator board. Bus terminaing located in lower left quadrant of E8/C
2. Tie floating input gates to GND.
3. Add 0.1uf decoupling capacitor to the underside of PCB between +5v and GND
4. J1 and J2 are card edge etched onto PCB
5. Numbers in blue refer to original EDUC-8 Card edge connection



Refdes	Part No	Description
U2B	74LS05	Hex inverter OC
U4B	74LS05	Hex inverter OC
U4C	74LS05	Hex inverter OC
U6C	74LS04	Hex inverter
U5C	74LS01	Quad NAND OC

Reference(s)	Value
C1	47nF
C2	47nF
C3	47nF
C4	47nF
H1	Card_handle
H2	Card_handle
J1	16-way-pcb-edge-Educ-8-connectors
J2	16-way-pcb-edge-Educ-8-connectors
R1	820
R2	2K2
R3	2K2
R4	2K2
U1	74LS95 (or 7495)
U2	74LS05
U3	74LS95
U4	74LS05
U5	74LS01
U6	74LS04
U7	74LS00
U8	74LS10
U9	74LS00
U10	74LS01



E8-A Accumulator

Sheet: /
File: Accumulator.kicad_sch

Title: EDUC-8A

Size: A3	Date: 2019-08-04	Rev: 1.0
KiCad E.D.A. kicad 7.0.11-rc3		Id: 1/1