

Part No.	Gate	Input pins	Out
U2	A	1, 2, 13, 12	NAND
U4	B	3, 4, 5, 6	NAND
U4	A	1, 2	INVERTER
U4	C	5, 1	INVERTER

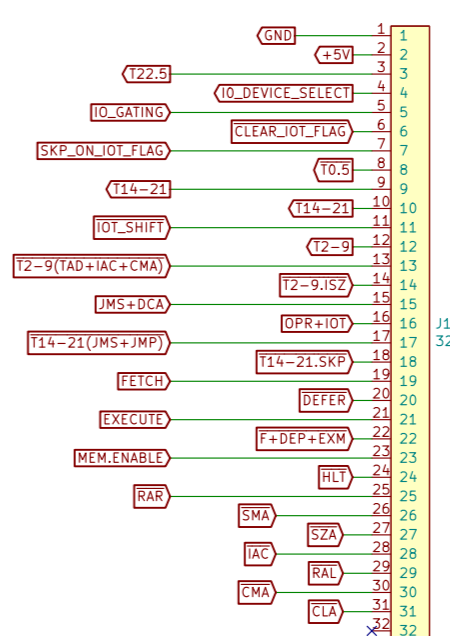
1. Inputs for all unused gates connected to GND

RefDes	Part No.	Supplier
U1	74LS00	
U2	74LS10	
U3	74LS04	
U4	74LS10	
U5	74LS00	
U6	74LS20	
U7	74LS00	
U8	74LS259	
U9	74LS00	
U10	74LS00	
U11	74LS00	
U12	74LS00	
U13	74LS00	
U14	74LS04	
U15	74LS04	
U16	74LS04	
U17	74LS04	
U18	74LS04	
U19	74LS04	
U20	74LS04	
U21	74LS04	
U22	74LS04	
U23	74LS04	
U24	74LS04	
U25	74LS04	
U26	74LS04	
U27	74LS04	
U28	74LS04	
U29	74LS04	
U30	74LS04	
U31	74LS04	
U32	74LS04	

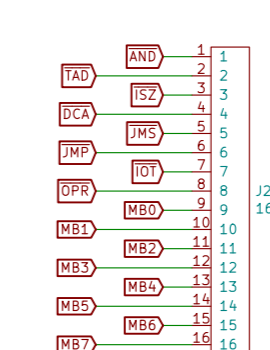
Part No.	Type	QTY
74LS00	Quad NAND	8
74LS10	Hex Inverter	2
74LS10	3 Input NAND	2
74LS10	6 Input NAND	1
74LS259	Addressable 8 bit latch	1
C1-C14	100nF 50v Polyester	14
C15-C16	100uF 35v electro	2
R1	2.2K 1/4 Watt resistor	1

Pin No	CARD_EDGE 1	DB25	Pin No
1	GND	1	1
2	+5V DC	1	14
3	FEEDBACK	1	2
4	IO_DEVICE_SELECT	1	16
5	IO_GATING	1	3
6	CLEAR_IOT_FLAG	1	16
7	SKP_ON_IOT_FLAG	1	4
8	ISZ	1	17
9	TAD+2I	1	5
10	TAD+2I	1	18
11	IOT_SHIFT	1	6
12	DEFER	1	19
13	TAD+IAC+CMA	1	7
14	T2-9 ISZ	1	20
15	JMS+DCA	1	8
16	OPR+IOT	1	21
17	T14-21(JMS+JMP)	1	9
18	T14-21(SKIP)	1	22
19	FEEDBACK	1	10
20	DEFER	1	23
21	EXECUTE	1	11
22	F+DEP+EXW	1	24
23	MEM_ENABLE	1	12
24	HLT	1	25
25	RAR	1	13

Pin No	CARD_EDGE 2	DB25	Pin No
A	AND	2	17
B	TAD	2	5
C	ISZ	2	18
D	DCA	2	2
E	JMS	2	19
F	JMP	2	7
G	IOT	2	20
H	OPR	2	8
I	MB0	2	21
K	MB1	2	9
L	MB2	2	22
M	MB3	2	10
N	MB4	2	23
P	MB5	2	11
Q	MB6	2	24
R	MB7	2	12
S	+5V	2	25
T	GND	2	13



J1 32-way-pcb-edge-Educ-8-connectors



J2 16-way-pcb-edge-Educ-8-connectors

INSTRUCTION DECODER